Date Mailed: November 6, 2	Date	Mailed:	November	6.	2003
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FORM 1449*	INFORMATION DISCLOSURE STATEMENT	Docket Number: KOLS.063PA	Application Number:	10 25	
IN AN APPLICATION (Use several sheets if necessary)		Applicant: FABRITIUS et al.			
		Filing Date: 11/06/2003	Group AR Unit: UNASSIG	SNED	

			U.S. PATENT DOCUMEN	rs			
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING	DATE OPRIATE
Mc	5,875,119	2/23/1999	BAUMAN et al.	702	187	1/28/	1997
MC.	5,164,969	11/17/1992	ALLEY et al.	377	39	7/12/	1991
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	DOCUMENT NO.		COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
MC	JP 2001-051972	23.02.2001	JP			ABSTRACT	
Mc EP 0 901 063 A2		10.03.1999	EP			N/A	
		ER DOCUMENTS	(Including Author, Title, Dat	e, Pertinent Pages	s, Etc.)		
Me FE	EB. 2000 "Circuitry a February 2	nd Method for Ev	aluating the Speed of ASIC S	Semiconductor De	vice Circuitry", Re	esearch Disclosur	e 430190,
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EXAMINER Hohamed When	DATE CONSIDERED 2/11/05				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.					